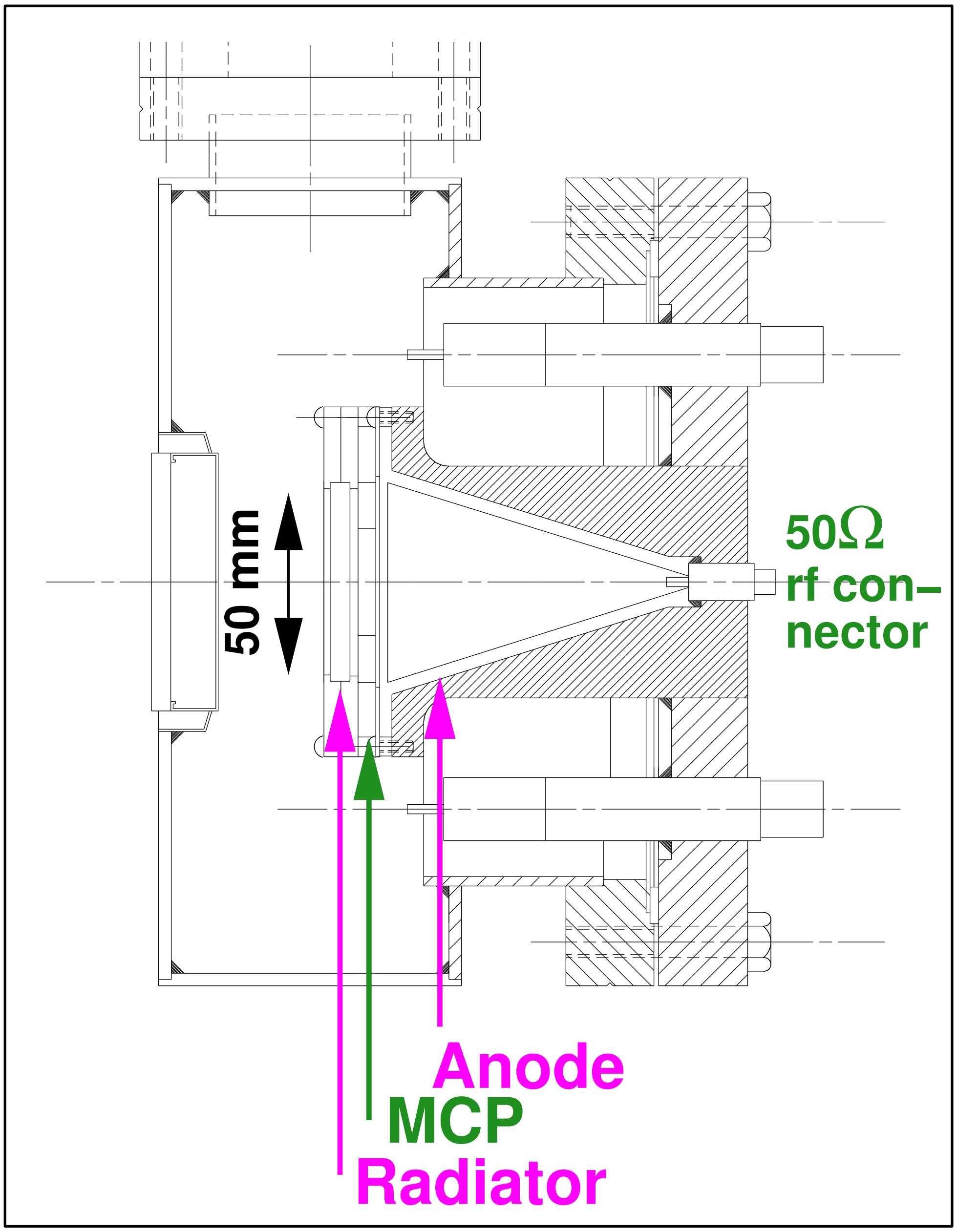
FAST TIMING R&D

FNAL, Princeton, UCLA

- Develop a fast timing device using a MgF_2 Cherenkov radiator, CsI photocathode, state–of–the–art microchannel plate multiplier, RF output connectors –> σ_t = 10 – 20 ps.



Overall mech–anical design complete (detail–ing to be done)

Output anode design under–stood

MCP with 18 mm small pore pixels exists

Radiator + photocathode + MCP + anode + rf connector test being prepared.